

Received November 4, 2020, accepted November 17, 2020, date of publication November 24, 2020, date of current version December 10, 2020.

Digital Object Identifier 10.1109/ACCESS.2020.3040323

Shortest Path Search Algorithm in Optimal Two-Dimensional Circulant Networks: Implementation for Networks-on-Chip

EMILIA A. MONAKHOVA¹, ALEKSANDR YU ROMANOV², (Member, IEEE),
AND EVGENII V. LEZHNEV², (Member, IEEE)

¹Institute of Computational Mathematics and Mathematical Geophysics, Siberian Branch of the Russian Academy of Sciences, 630090 Novosibirsk, Russia

²Higher School of Economics, National Research University, 101000 Moscow, Russia

Corresponding author: Aleksandr Yu Romanov (a.romanov@hse.ru)

The work of Aleksandr Yu Romanov and Evgenii V. Lezhnev was supported by the Basic Research Program of the National Research University Higher School of Economics. The work of Emilia A. Monakhova was supported in part by the Institute of Computational Mathematics and Mathematical Geophysics, Siberian Branch of the Russian Academy of Sciences (SB RAS), under Project 0315-2019-0006.

ABSTRACT For a family of optimal two-dimensional circulant networks with an analytical description, two new improved versions of the shortest path search algorithm with a constant complexity estimate are obtained. A simple, based on the geometric model of circulant graphs, proof of the formulas used for the shortest path search algorithm is given. Pair exchange algorithms are presented, and their estimates are given for networks-on-chip (NoCs) with a topology in the form of the considered graphs. New versions of the algorithm improve the previously proposed shortest path search algorithm for optimal generalized Petersen graphs with an analytical description. The new proposed algorithm is a promising solution for the use in NoCs which was confirmed by an experimental study while synthesizing NoC communication subsystems and comparing the consumed hardware resources with those when other previously developed routing algorithms.

INDEX TERMS Two-dimensional circulant graphs, diameter, shortest path, optimal generalized Petersen graphs, networks-on-chip.

I. INTRODUCTION

Along with the wide interest in circulant networks in various fields of computer science and computer engineering [1]–[4], their application as network-on-chip (NoC) topologies [5] is becoming relevant. This is due to their best structural characteristics [6] and high scalability with a large number of nodes compared to standard NoC topologies (mesh, torus). In connection with the need to reduce the hardware cost of resources for NoCs, an important task is the development of effective routing algorithms in networks with circulant topology. This paper discusses efficient pair routing algorithms designed for two-dimensional circulant networks.

Let s_1, s_2, \dots, s_l, N be integers such that $1 \leq s_1 < s_2 < \dots < s_l < N$. An undirected graph C with a set of vertices $V = \{0, 1, \dots, N-1\}$ and a set of edges $E = \{(i, j) : i - j \equiv \pm s_m \pmod{N}, m = \overline{1, l}\}$ is called a

The associate editor coordinating the review of this manuscript and approving it for publication was Walter Didimo ¹.

circulant network. Parametric description of type $(N; S)$ defines a circulant of order N and dimension l with a set of generators $S = (s_1, s_2, \dots, s_l)$. The symmetry property of circulants allows us to restrict ourselves to considering circulant graphs with generators not exceeding $\lfloor N/2 \rfloor$.

The diameter D of a graph is equal to $D = \max_{i, j \in V} D(i, j)$, where $D(i, j)$ is the length of the shortest path between vertices i and j . The average distance \bar{D} is equal to $\bar{D} = (1/(N(N-1))) \sum_{i, j} D(i, j)$. The diameter and average distance estimate maximum and average transmission delays in the network. As studies have shown, the best structures of computing systems, according to various criteria of functioning (reliability, connectivity, bisection bandwidth) with the same number of nodes and communication lines, are structures with the minimum diameter and average distance.

In this paper, we consider two-dimensional circulant networks. Such networks are represented by undirected circulant graphs $C(N; s_1, s_2)$ with N vertices labeled with the integers

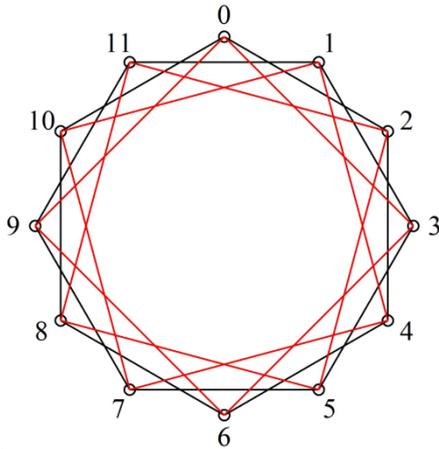


FIGURE 1. Two-dimensional circulant graph $C(12;2,3)$.

modulo N , and each vertex i is joined by an edge to the vertices $(i \pm s_1) \bmod N$ and $(i \pm s_2) \bmod N$. Fig. 1 shows an example of such graph.

When using circulant graphs as communication networks of massively parallel computers or networks-on-chip, an urgent issue is an effective solution to the problem of organizing routing algorithms in them. In pair (two-terminal) routing, a message is to be routed from its source node to its destination node according to the model applied. Further in the article, we assume the undirected store-and-forward model. The organization of pair exchanges requires determination of the shortest paths in a graph. The known Dijkstra's algorithm of finding the shortest paths in any connected graph of order N has both time and space quadratic complexity $O(N^2)$ which is the number of elements in the adjacency matrix. In [7] the authors proved that the shortest path problem is NP-hard if a description $(N; S)$ of a circulant graph of arbitrary dimension l is used. For fixed $l = 2$, different pair routing algorithms were obtained for circulant graphs in [5], [8]–[14]. There are several approaches to solve the two-terminal routing problem in circulants of dimension two. The first approach uses a construction of routing tables. Such algorithms take constant time to determine the next node on the shortest path of the packet, but require the extra space for the routing tables of the order $O(N^2)$ which is not acceptable for NoCs. The second approach is based on the $O(\log N)$ -time preprocessing calculating routing parameters and also requires the extra space to store the additional information. In the third approach, the dynamic algorithms are capable (without routing tables or preprocessing) to route the packet along the shortest path using only the name of the packet destination node. They are more flexible in case of traffic congestions or node/link faults, but the best known time complexity of dynamic algorithms is only $O(\log N)$. According to engineering approach, in [5], [14] the search for the optimal routing algorithm in NoCs with two-dimensional circulant topology is implemented. It is based on the understanding of structural organization of NoCs and desire to offer simple one-hop routing algorithms with a view to reducing

TABLE 1. Description of key notations and abbreviations used in this paper.

Notation	Description
N	number of nodes in a network
l	dimension of a circulant
$S = (s_1, s_2, \dots, s_l)$	set of circulant generators
$C(N; s_1, s_2, \dots, s_l)$	circulant graph with N nodes and l generators
D	network diameter
$D(i, j)$	distance between the nodes i and j
\bar{D}	average internode distance in a network
$lb(N)$	exact lower bound of the diameter in $C(N; s_1, s_2)$
N_d	maximal number of nodes in $C(N; s_1, s_2)$ of diameter d
$A^{0k} = (x^{0k}, y^{0k})$	shortest path vector from node 0 to node k
$P(N, a, b)$	generalized Petersen graph with $2N$ nodes and generators $(1, 2a, 2b)$
$C(N; d, d + 1)$	optimal two-dimensional circulant graph (network)
REG	number of occupied memory resources in NoC
ALM	number of occupied logical blocks in NoC
PEA	new routing algorithm (Pair exchange algorithm)

hardware costs and at the same time providing an acceptable distance between the nodes.

In this paper, the another approach based on analytical and mathematical methods of graph analysis was chosen; i.e., it is proposed to first solve the routing problem analytically with the time complexity $O(1)$ independent of the network size and then adapt the solution for practical use in NoCs.

For the convenience, Table 1 lists the notations used in this paper.

II. OPTIMAL TWO-DIMENSIONAL CIRCULANTS

Two-dimensional circulants are intensively studied in the literature in connection with various practical applications [1]–[3]. The exact lower bound of the diameter of two-dimensional circulants for any order $N > 4$ is $lb(N) = \lceil (-1 + (2N - 1)^{1/2})/2 \rceil$ [15].

Bermond *et al.* [16], Boesch and Wang [17], Yebra *et al.* [18] showed that $lb(N)$ can be achieved by taking $s_1 = lb(N)$, $s_2 = lb(N) + 1$. In 1981, the author of work [15] proved that for every N , circulant graphs can achieve simultaneously both minimum diameter $lb(N)$ and minimum possible average distance (i.e., to be optimal).

Theorem 1: For every $N > 4$, the optimal two-dimensional circulant of order N is

$$C(N; d, d + 1), \quad \text{where } d = \lceil (-1 + (2N - 1)^{1/2})/2 \rceil, \quad (1)$$

here $\lceil x \rceil$ is the nearest integer to x .

In 1991, Beivide *et al.* [19] obtained the same circulant family (1) written in a different form.

Theorem 2: For every $N > 2$, the optimal two-dimensional circulant of order N is

$$C(N; b-1, b), \quad \text{where } b = \lceil (N/2)^{1/2} \rceil. \quad (2)$$

The two optimal families (1) and (2) are identical both in numerical values of generators and orders, as well as in the ranges to be optimal with these generators. Let us prove it.

Lemma 1: For every order $N > 4$, circulant graphs $C(N; d, d + 1)$ and $C(N; b - 1, b)$, described by (1) and (2), respectively, coincide for $b - 1 = d$.

Proof: Let $N > 4$ be any given number. For family (1), the definition of the nearest integer implies $d - \frac{1}{2} \leq ((2N - 1)^{\frac{1}{2}} - 1)/2 < d + 1/2$. Hence, after simple transformations, we obtain $2d^2 + 1/2 \leq N < 2(d + 1)^2 + 1/2$. Since N is an integer, we have

$$2d^2 < N \leq 2(d + 1)^2.$$

Similarly, for family (2), it follows from the definition of b : $b - 1 < (N/2)^{1/2} \leq b$ and

$$2(b - 1)^2 < N \leq 2b^2.$$

Since the obtained ranges of variation of N take place for any given N , this implies $b - 1 = d$. The lemma is proved.

Research issues of the found family of optimal circulants as communication networks of computer systems have been extensively studied. Liestman *et al.* [20] studied network features of such circulants in particular optimal graphs of type $C(N_d; d, d + 1)$ and considered the possibility of embedding of lattices into them. Hereinafter, $N_d = 2d^2 + 2d + 1$ is the maximum possible order of a two-dimensional circulant of diameter $d \geq 1$. In [21]–[23] the authors considered optimal networks (2) called Midimew networks (mesh-connected networks with wrap-around links) as a technical implementation of communication networks of supercomputer systems. Puente *et al.* [21] showed that under real loads for Midimew networks, there is an increase in network performance and a decrease in the length of average communication path compared to tori. In [21], [22] a practical solution to the problem of preventing deadlocks (path locks during packet transmission) was presented. Yang *et al.* [23] used several examples of such graphs as a basis in the design of communication networks of parallel systems. Martinez *et al.* [13], [24] applied circulant networks, in particular a family of type $C(N_d; d, d + 1)$, in coding theory when constructing perfect group codes. The implementation of the graphs described by (1) or (2) was proposed as a topology in the design of supercomputers with mass parallelism and NoCs [5], [23], [25]. A number of new hierarchical network designs were also constructed using the Minimum Distance Mesh with Wrap-around links (Midimew networks) as elements [26], [27]; simple use of circulant (Midimew) topologies instead torus topology in TESH [28] topology notably improved network performance parameters [29].

The analytical solution to the problem of finding the shortest paths by a parametric description for optimal two-dimensional circulant graphs described by (1) and (2) was proposed in [30] and by Beivide *et al.* [19] and Puente *et al.* [22]. In the present study, the above mentioned analytical solution was improved and corrected, as well as used to develop universal dynamic pair routing algorithm applied for the routing in NoCs with two-dimensional circulant topologies.

III. FINDING THE SHORTEST PATHS IN TWO-DIMENSIONAL CIRCULANTS

Let us consider an effective solution to the problem of finding the shortest paths by a parametric description for optimal circulant graphs having descriptions of type $C(N; d, d + 1)$, where d is determined by (1).

Let $A^{0k} = (x^{0k}, y^{0k}) = (x, y)$ denote a vector of the shortest path from vertex 0 to vertex k , $0 \leq k < N$ in a circulant graph $C(N; s_1, s_2)$. Here $|x^{0k}|$ sets the number of generators s_1 ; $|y^{0k}|$ sets the number of generators s_2 included in the shortest path from 0 to k ; $\text{sgn}x^{0k}$ and $\text{sgn}y^{0k}$ determine the directions of motion in the shortest path along (+) or against (−) of the corresponding generator. We show that for these graphs, the relationship between the numbers of vertices i and j , and vector of shortest path A^{ij} from i to j can be obtained analytically.

To do this, we need to consider the geometric model of a graph $C(N; d, d + 1)$ [15]. This graph is constructed on a Z^2 plane as a rhomboid configuration from unit squares of an integer lattice, where each point of the lattice (x, y) is labeled by a number $k = xd + y(d + 1) \bmod N$, where $0 \leq k < N$ is a vertex number of the graph. All vertex marks $0 \leq k < N$ are repeated on the plane an infinite number of times forming a dense packing of rhomboid-like configurations of unit squares. Fig. 2 shows how, from a rhombus with the number of vertices $N = N_d$ (indicated by a solid line), all optimal configurations with the number of vertices $2d^2 - 1 \leq N \leq 2(d + 1)^2 + 1$, by building up (Fig. 2a) and contraction (Fig. 2b) of cells (vertices) on the last layer, are obtained. All vertices of a graph $C(N; d, d + 1)$ of the diameter

$$D = \begin{cases} d, & \text{if } 2d^2 - 1 \leq N \leq N_d, \\ d + 1, & \text{if } N_d < N \leq 2(d + 1)^2 + 1, \end{cases}$$

are located inside the rhombus $R_D = \{(x, y) \mid |x| + |y| \leq D\}$, and the layers from 0 to $D - 1$ are completely filled, and among the numbers of the vertices located on these layers, there are not the same numbers. By virtue of the indicated features, for vertices $0 \leq k < N$ of a graph $C(N; d, d + 1)$ of diameter D , we have the following: to define the vector of shortest paths from 0 to k , it suffices to calculate the paths in the rhombus R_D .

As an example of the geometric representation of graphs of the family $C(N; d, d + 1)$, Fig. 3 shows the graph $C(50; 4, 5)$ of diameter $D = 5$ in the coordinate system (x, y) . For simplicity of the image, the wrap-around links (edges) in the graph are not shown. The graph $C(50; 4, 5)$ has the maximum possible order among the graphs of family (1) with generators $s_1 = 4, s_2 = 5$. Note that the graph $C(51; 4, 5)$ is also optimal.

Theorems 3 and 4 solve the problem of computing the vector of shortest paths from a zero vertex to all vertices of the graphs of family (1). Moreover, in the proof of Theorem 3, division of the vertex number of the graph by generator s_1 is used which corresponds to selected horizontal regions of the graph in Fig. 3a; in the proof of Theorem 4, division by

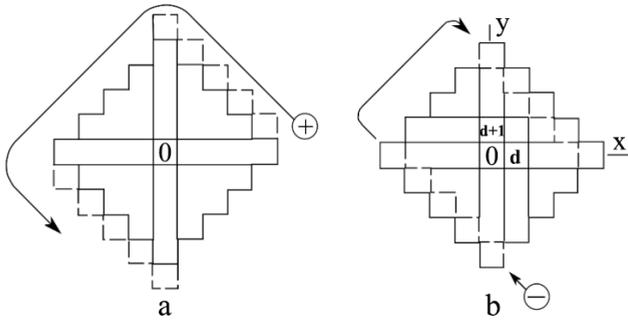


FIGURE 2. Geometric model of graphs $C(N; d, d + 1)$.

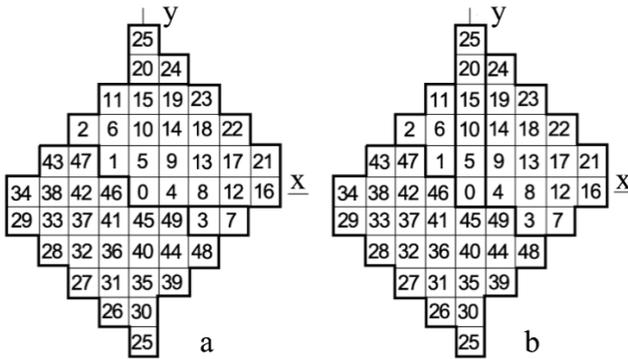


FIGURE 3. Graph $C(50; 4, 5)$.

generator s_2 is used which corresponds to the selected vertical regions of the graph in Fig. 3b.

Theorem 3: Let $0 \leq k < N$ be a vertex number in a graph $C(N; d, d + 1)$. Then the coordinates of vector $A^{0k} = (x^{0k}, y^{0k})$ are calculated as follows:

when $k \leq \lfloor N/2 \rfloor$

$$(x^{0k}, y^{0k}) = \begin{cases} (\alpha, \beta), & \text{if } \beta - d \leq \alpha \leq d \\ (\alpha + d + 1, \beta - d), & \text{if } \alpha < \beta - d \\ (\alpha - (d + 1), \beta + d) & \text{otherwise,} \end{cases} \quad (3)$$

where $\beta = k \bmod d, \alpha = \lfloor k/d \rfloor - \beta,$
when $k > \lfloor N/2 \rfloor$

$$(x^{0k}, y^{0k}) = (-x^{0N-k}, -y^{0N-k}).$$

Proof: According to the property of circulants $(x^{0k}, y^{0k}) = (-x^{0N-k}, -y^{0N-k})$, for any vertex $k \in \overline{0, N - 1}$. Thus, definition of the shortest path vector A^{0k} for all $\lfloor N/2 \rfloor < k < N$ reduces to definition of the shortest path vector of the vertex with number $(N - k)$ (the vertex lies in the upper part of the rhombus to the right of the lines $y = -x$ for $x \leq 0$ and $y = -x + 1$ for $x > 0$) and replacing the signs of received coordinates with the opposite ones. Now let $0 \leq k \leq \lfloor N/2 \rfloor$. It is required to find $x^{0k} = x, y^{0k} = y$ such coordinates of the vertex with number k that

$$k = xd + y(d + 1). \quad (4)$$

In the proof, we use the fact that for any positive integers k and d , number k is uniquely representable in terms of the integer part and remainder of the division by d :

$$\begin{aligned} k &= \lfloor k/d \rfloor d + k \bmod d, \\ \lfloor k/d \rfloor &\geq 0, \quad 0 \leq k \bmod d < d. \end{aligned} \quad (5)$$

1) Consider the region of rhombus R_D representing a graph $C(N; d, d + 1)$ and bounded by the following lines:

$$\begin{cases} 0 \leq y < d, \\ -y \leq x \leq d + 1 - y, \\ y - d \leq x \leq d. \end{cases}$$

In this region of rhombus,

$$\begin{cases} x + y \geq 0, \\ 0 \leq y < d. \end{cases}$$

Thus, if we represent number k of the vertex from this region written in the form (4) as $k = d(x + y) + y$, then, due to the uniqueness of expression (5) for k , we obtain

$$\lfloor k/d \rfloor = x + y, \quad k \bmod d = y.$$

This implies

$$x = \lfloor k/d \rfloor - k \bmod d, \quad y = k \bmod d.$$

Substituting the found expressions for x and y in the record of lines bounding the region under consideration, we find that the formulas found for x and y hold when

$$k \bmod d - d \leq \lfloor k/d \rfloor - k \bmod d \leq d.$$

2) Now consider the region of the rhombus bounded by the lines

$$\begin{cases} y < 0, \\ -y + 1 \leq x \leq d + y. \end{cases}$$

In this region of rhombus,

$$\begin{cases} x + y - 1 \geq 0, \\ 0 < d + y < d. \end{cases}$$

Thus, if we represent number k of the vertex from this region written in the form (4) as

$$k = (x + y - 1)d + (d + y),$$

then, due to the uniqueness of expression (5) for k , we obtain

$$\lfloor k/d \rfloor = x + y - 1, \quad k \bmod d = d + y.$$

This implies

$$x = \lfloor k/d \rfloor - k \bmod d + (d + 1), \quad y = k \bmod d - d.$$

Similarly to case 1, we find that the formulas found for x and y hold when $\lfloor k/d \rfloor - k \bmod d < k \bmod d - d$.

3) For vertices with numbers $k = d^2 + d, d^2 + 2d, d^2 + 2d + 1$, the coordinates are $(0, d), (1, d), (0, d + 1)$, respectively. The coordinates (x^{0k}, y^{0k}) calculated by formula (3) coincide with the data. These formulas hold when

$$\lfloor k/d \rfloor - k \bmod d > d.$$

The theorem is proved.

Note that in graphs $C(N; d, d + 1)$ for $2d^2 < N < Nd$, to calculate the vector of shortest paths from 0 to any vertex, it is sufficient to use only the first two types of formulas from (3). Theorem 3 gives an analytical solution to the problem of determining the shortest paths vector by the number of the vertex of a graph $C(N; d, d + 1)$ with respect to the zero vertex. Thus, calculation of the shortest paths in the structures of the considered description turns out to be simpler than the similar procedure for two-dimensional circulants with a description different from that mentioned above [8].

Another type of formulas that can be used in calculating the shortest path vectors in circulant graphs $C(N; d, d + 1)$ is determined by the following theorem.

Theorem 4: Let $0 \leq k < N$ be a vertex number in a graph $C(N; d, d + 1)$. Then the coordinates of vector $A^{0k} = (x^{0k}, y^{0k})$ are calculated as follows:

for $k \leq \lfloor N/2 \rfloor$,

$$\begin{aligned} & (x^{0k}, y^{0k}) \\ &= \begin{cases} (-\beta, \alpha), & \text{if } \beta = 0 \text{ or } \alpha + \beta < d + 1, \\ (d + 1 - \beta, \alpha - d), & \text{otherwise,} \end{cases} \end{aligned} \quad (6)$$

where $\beta = k \bmod (d + 1), \alpha = \lfloor k/(d + 1) \rfloor + \beta$; for $k > \lfloor N/2 \rfloor$,

$$(x^{0k}, y^{0k}) = (-x^{0N-k}, -y^{0N-k}).$$

Proof: By analogy with the proof of Theorem 3, the definition of vector A^{0k} for all $\lfloor N/2 \rfloor < k < N$ reduces to determining the vector of the shortest paths of the vertex with number $(N - k)$ and replacing the signs of resulting coordinates with the opposite ones.

Now let $0 \leq k \leq N/2$. It is required to find $x^{0k} = x, y^{0k} = y$ such coordinates of the vertex with number k that

$$k = xd + y(d + 1). \quad (7)$$

For the proof, we will use the fact that for any positive integers k and $d + 1$, number k is uniquely representable in terms of the integer part and remainder of the division by $d + 1$:

$$\begin{aligned} k &= \lfloor k/(d + 1) \rfloor (d + 1) + k \bmod (d + 1), \\ \lfloor k/(d + 1) \rfloor &\geq 0, \quad 0 \leq k \bmod (d + 1) < d + 1. \end{aligned} \quad (8)$$

1) For vertices with numbers $k = i(d + 1)$, the coordinates are

$$x = 0, y = \lfloor k/(d + 1) \rfloor.$$

2) Consider the region of the rhombus R_D representing a graph $C(N; d, d + 1)$ and bounded by the following lines:

$$\begin{cases} x < 0, \\ -x \leq y \leq x + d. \end{cases}$$

In this region of the rhombus,

$$\begin{cases} x + y \geq 0, \\ 0 < -x < d. \end{cases}$$

Thus, if we represent number k of the vertex from this region written in the form (7) as $k = (x + y)(d + 1) + (-x)$, then due to the uniqueness of expression (8) for k , we obtain

$$\lfloor k/(d + 1) \rfloor = x + y, k \bmod (d + 1) = -x.$$

This implies

$$\begin{aligned} x &= -k \bmod (d + 1), \\ y &= \lfloor k/(d + 1) \rfloor + k \bmod (d + 1). \end{aligned}$$

Substituting the found expressions for x and y in the record of lines bounding the rhombus region under consideration, we find that the formulas found for x and y hold when

$$\lfloor k/(d + 1) \rfloor < d + 1 - 2k \bmod (d + 1).$$

Thus, the kind of formulas found for case 2 also holds for case 1 under the condition

$$k \bmod (d + 1) = 0.$$

3) Let us now consider the region of the rhombus bounded by lines

$$\begin{cases} 0 < x < d + 1, \\ x - d \leq y, \\ 1 - x \leq y \leq d + 1 - x. \end{cases}$$

In this region of the rhombus,

$$\begin{cases} x + y - 1 \geq 0, \\ 0 < d + 1 - x < d + 1. \end{cases}$$

Thus, if we represent number k of the vertex from this region written in the form (7) as $k = (x + y - 1)(d + 1) + (d + 1 - x)$, then, due to the uniqueness of expression (8) for k , we obtain

$$\begin{aligned} \lfloor k/(d + 1) \rfloor &= x + y - 1, \\ k \bmod (d + 1) &= d + 1 - x. \end{aligned}$$

This implies

$$\begin{aligned} x &= -k \bmod (d + 1) + d + 1, \\ y &= \lfloor k/(d + 1) \rfloor + k \bmod (d + 1) - d. \end{aligned}$$

Similarly to case 2, we find that the formulas found for x and y hold under the condition

$$d + 1 - 2k \bmod (d + 1) \leq \lfloor k/(d + 1) \rfloor.$$

The theorem is proved.

Note that in circulant topologies, the application of the shortest path vector $A^{0k} = (x^{0k}, y^{0k})$ for development of routing algorithms offers a possibility to use a total number of different shortest paths defined by this vector. More precisely, a total number of all the shortest paths for A^{0k} is $(|x^{0k}| + |y^{0k}|)! / (|x^{0k}|! |y^{0k}|!)$. Thus, this property makes it possible to construct an adaptive dynamic pair exchange algorithm avoiding potential routing problems (traffic congestions or node/link faults).

IV. PAIR EXCHANGE ALGORITHM IN TWO-DIMENSIONAL CIRCULANTS

Consider the path procedure that implements pair interactions (pair exchange) in a NoC with an optimal two-dimensional circulant topology described by (1). Suppose, it is required to transfer a packet from a source node s to a destination node j . Provided that the shortest path vector A^{ij} (of a destination node j with respect to a node i executing the path procedure) is known in each transit node i , modification of the shortest path vector to determine the output direction belonging to the shortest path to the destination node is reduced to operation

$$\begin{aligned} (x_p^{ij})' &= x_p^{ij} - \text{sgn}(x_p^{ij}) \cdot 1 \\ \text{for any } x_p^{ij} &\neq 0, \quad x_p^{ij} \in \{x^{ij}, y^{ij}\}. \end{aligned} \quad (9)$$

If there are no failures of the nodes and communication lines in a network, then the vector of the shortest paths between the nodes will be calculated in the source node by the number of the destination node and written to the packet header. In each transit node along the path to the destination, any of the directions belonging to the shortest path is selected, and the vector of the shortest paths is modified by operation (9) in order to reduce the absolute value of nonzero coordinates of the vector. The modified vector is written to the packet header which is passed on. The end of the path procedure is the equality to zero of all coordinates of vector of the shortest paths.

The data for the pair exchange algorithm can be described as follows: A^{ij} is a vector of the shortest paths of the destination node with number j relative to a node i executing the path procedure; W is a set of numbers of output directions belonging to the shortest path to the destination node; the entry $\text{inp}_0 > 0$ means that a packet with the sign ‘‘pair exchange’’ has arrived at the input port with number 0 (that is, from the source node); the entry $\text{inp}_h > 0$ means that from a neighboring node, the packet with the sign ‘‘pair exchange’’ has arrived at the input port with number h , ($h = \overline{1, 4}$); the entry ‘‘put the packet in the output port out_p ’’ means that the packet is put in the output port with number p .

Depending on the chosen method of calculating the shortest path vector A^{0k} and using formulas (3) or (6), two versions of the algorithm are obtained: version (1) and version (2), respectively.

We give estimates of this algorithm. NoC with the number of nodes N requires $\lceil \log_2 N \rceil + \lceil \log_2 (N/2)^{1/2} \rceil$ bits to store in memory the values of two parameters: N and generator

Algorithm 1 (Pair Exchange Algorithm)

Input: parameters N, d for version (1), or $N, d + 1$ for version (2); j is the node number of the receiver; i is the number of the node performing the path procedure (in the source node $i = s$).

Output: modified vector of the shortest paths A^{ij} .

- 1: **If** $\text{inp}_0 > 0$ **then**
 $k := |s - j|$ and go to 5.
- 2: **If** $\text{inp}_h > 0$ **then**
- 3: **If** $A^{ij} = 0$ **then**
 go to 9,
- 4: **else** go to 8.
- 5: **If** $j \leq s$ **then**
 $\text{sgn} := -1$ and go to 7,
- 6: **else** $\text{sgn} := 1$.
- 7: **If** $k > \lfloor N/2 \rfloor$ **then**
 $\text{sgn} := -\text{sgn}$ and $k := N - k$.
 Calculate A^{0k} by the formulas (3) or (6),
 $A^{ij} := \text{sgn} \cdot A^{0k}$.
- 8: Choose any number $p \in W$, modify A^{ij} by operation (9),
 write A^{ij} in the packet header,
 put the packet in the output port out_p .
- 9: **End** of the algorithm.

TABLE 2. Overall time (t) of the execution of the shortest path algorithm in circulants of type $C(N; d, d + 1)$.

Number of nodes N	$T \cdot 10^{-6}$ (sec)	Number of nodes N	$T \cdot 10^{-6}$ (sec)
15000	1.3969	90000	1.2855
30000	1.2964	105000	1.3967
45000	1.2881	120000	1.3968
60000	1.3962	135000	1.2346
75000	1.3963	150000	1.2240

d for version (1), or generator $d + 1$ for version (2). Items 1 and 5-8 of the algorithm are executed in the source node. In total, for version (1), regardless of the number of nodes in the network, 1 modulo operation, 2 division operations, 2 multiplication operations, 19 operations (such as addition), and about 40 words of RAM are required. For version (2) of the algorithm, 5 operations (such as addition) less are required. In transit nodes, items 2-4, 8 are performed: 1 operation of multiplication and 3 operations (such as subtraction) and 10 words of RAM are required.

In order to determine the time complexity of the proposed pair exchange algorithm, we experimentally checked its realization with Python 3 programming language on Intel Core i5-8265 processor. We tested circulant networks with the number of nodes N from 20 to 150000 and obtained a constant time of execution of algorithm which proves our estimate. In Table 2, the overall time of execution of the shortest path algorithm is presented provided that the messages are delivered from node 0 to all the other $N - 1$ nodes.

The presented algorithm for finding the shortest paths in optimal two-dimensional circulants does not use routing

TABLE 3. Time complexity of different routing algorithms for two-dimensional circulants.

Algorithm	Graph	Time complexity	Optimal path
Pair exchange alg. (PEA)	$C(N; d, d + 1)$	$O(1)$	yes
Robic [8]	$C(N; s_1, s_2)$	$O(N^{1/2})$	yes
Dobravec et al. [9]	$C(N; s_1, s_2)$	$O(\log N)$	yes
Zerovnik et al. [10]	$C(N; s_1, s_2)$	$O(\log N)$	yes
Gomez et al. [11]	$C(N; s_1, s_2)$	$O(\log N)$	yes
Chen et al. [12]	$C(N; s_1, s_2)$	$O(2 \log N)$	yes
Martinez et al. [13]	$C(N_d; d, d + 1)$	$O(d)$	yes

tables and adjacency matrices; it is adapted to node and link failures and load distribution of nodes; it has a constant complexity $O(1)$ that does not depend on the size of the graph, in contrast to the following algorithms: 1) Dijkstra's algorithm with quadratic complexity $O(N^2)$ for any connected graph of order N ; 2) the algorithm from [8] with an estimate of $O(N^{1/2})$ of the time for calculating the shortest paths for circulants of type $C(N; s_1, s_2)$ and $l = O(d)$ of the routing steps, where l is the distance between the vertices, and d is the diameter of the network; 3) the algorithm from [9] having the overall time complexity of one routing step $O(\log N)$; 4) the algorithm from [10] with $O(\log N)$ —time preprocessing; 5) the algorithm from [11] with an estimate of $O(\log N)$ arithmetic operations; 6) the algorithm from [12] having a constant estimate of the time complexity to calculate the shortest path for circulants of type $C(N; s_1, s_2)$, but requiring preliminary calculation of the parameters for calculation with time complexity $O(2 \log N)$; 7) the algorithm from [13] in circulants of type $C(N_d; d, d + 1)$ with an estimate of $O(d)$.

The results of these algorithms comparison on time complexity are presented in Table 3.

Comparison of algorithms for such parameters as auxiliary space usage and intrinsic space usage and others is not required in this study, due to the specific of the required area of application of the developed algorithm for implementation at the register-transfer level (RTL) of routers in NoC.

V. IMPROVING THE SHORTEST PATH SEARCH ALGORITHM FOR GENERALIZED PETERSEN GRAPHS

Theorems 3 and 4 can be also used to improve the shortest path search algorithm developed for optimal generalized Petersen graphs [3]. Generalized Petersen graphs as regular graphs of degree three can increase network reliability, compared to ring networks and trees used in modern infrastructures, and are significantly better than chordal rings of degree three in a number of indicators including the diameter and average distance, while maintaining a low network cost. Thus, generalized Petersen graphs of degree three can also be considered as a promising topology for implementation in NoCs. We give the necessary definitions.

Generalized Petersen graphs $P(N, a, b)$ of order $2N$ are graphs consisting of outer (vertices V_0 connected by generator $2a$) and inner (vertices V_1 connected by generator $2b$) rings with an equal number of vertices N connected by edges.

The edges connecting vertices $2i$ and $2i + 1$, $i = \overline{0, N - 1}$ will be considered corresponding to generator $c = 1$. Graphs $P(N, a, b)$ with the smallest possible diameter for a given order are called the optimal ones.

An univalent mapping of the family of optimal two-dimensional circulants described by (1) was found in the class of generalized Petersen graphs with preserving the optimality of the graph, and a parametric description of optimal generalized Petersen graphs for any graphs of order $2N$ was obtained:

Theorem 5: For every $N > 9$, there exists an optimal generalized Petersen graph $P(N, a, a + 1)$, where $a = \left\lceil \left((N - 1)/2 \right)^{1/2} \right\rceil - 1$.

This makes it possible, on the basis of the shortest paths search algorithm [30] for optimal circulants described by (1), to find an analytical solution to the shortest paths search problem for the class of optimal generalized Petersen graphs.

The use of formulas (3) and (6) makes it possible to improve the shortest path search algorithm for generalized Petersen graphs in terms of required memory and speed, since the main part of the calculations in it is determination of the shortest path vector from 0 to any vertex of an appropriate circulant graph $C(N; d, d + 1)$.

VI. PAIR EXCHANGE ALGORITHM TESTING

The first version of Algorithm 1 was implemented for NoCs. For its operation on the FPGA, it is necessary that the router receives all the necessary data for calculating the route. Based on the proposed algorithm, the head fleet should contain two fields in which the number of steps along generators s_1 and s_2 (the shortest path vector $A^{\vec{t}}$) is stored. This is correct when the router sends the calculated path to the next router. But when receiving a packet from the IP-core, the destination node number should be stored in the head fleet. Thus, the load on the package in the general case is calculated by the following formula:

$$P = 2 \lceil \log_2 N \rceil,$$

where N is the number of nodes in the network.

Since the maximum number of hops does not exceed D (the diameter of a graph), we can assume that values of vector of the shortest paths transmitted in the head fleet will not exceed D either. Thus, the load on the package can be reduced to $2 \lceil \log_2 D \rceil$, provided that $\lceil \log_2 N \rceil \leq 2 \lceil \log_2 D \rceil$. The number of such circulants does not exceed 54 % of the total number of possible options for $N \leq 256$ with the memory gain in 1–2 bits; and for $N > 256$, such circulants do not occur at all.

The router stores its network number, total number of routers in the network and the value of generator s_1 , since s_2 can be easily calculated. Additionally, the router stores the values calculated during the execution of algorithm. The total size of the stored data can be estimated using the following formula:

$$M_{all} = 6 \lceil \log_2 N \rceil + \lceil \log_2 (N/2) \rceil + 1,$$

where N is the number of nodes in the network;

TABLE 4. Calculation and RTL synthesis data for the pair exchange algorithm.

Circulant	Number of nodes	Diameter	1 router bit	Whole network bit	1 router REG	Whole network REG	1 router ALM	Whole network ALM
$C(9; 2, 3)$	9	2	27	243	27	371	104	732
$C(16; 2, 3)$	16	3	28	448	33	757	142	1825
$C(25; 3, 4)$	25	3	35	875	33	1158	143	2903
$C(36; 4, 5)$	36	4	42	1512	39	1962	175	5473
$C(49; 4, 5)$	49	5	42	2058	39	2654	175	7899
$C(64; 5, 6)$	64	6	42	2688	45	3965	216	12909
$C(81; 6, 7)$	81	6	49	3969	45	4904	218	16755
$C(100; 7, 8)$	100	7	49	4900	45	6044	218	19226

$\lceil \log_2 N \rceil$ is the required amount of memory in bits to store the serial number of the router in the network, total number of routers in the network, and values calculated by the algorithm;

$\lceil \log_2 (N/2) \rceil$ is the required amount of memory in bits to store generator s_1 ;

1 bit is the required amount of memory to store the flag of generator selection for transmitting the packet to the next node.

Testing the operation of the proposed algorithm for the circulants of type $C(N; d, d+1)$ was carried out on the FPGA Cyclone V5CGXFC9A6U19I7 [31] by Intel FPGA. The routers are described in Verilog. For testing, the optimal circulants of type $C(N; d, d+1)$ with number of nodes determined by formula $N = n^2$, where N is the number of nodes in the network, n is a natural number, are chosen. Table 4 shows the results of the theoretical calculation of occupied memory in bits, as well as consumed resources REG and ALM obtained after the synthesis of NoC communication subsystem.

The results show that the amount of required memory and ALM blocks occupied by one router changes in steps. This is due to increase in data size for the stored parameters and does not directly depend on the number of nodes in the network. The dependence can also be observed with the use of resources for the entire network – for those circulants, in which the use of registers by one router does not change, the increase in resource use by the entire network is less.

There is also a discrepancy of 20–30 % in a larger direction of REG consumption in comparison with theoretical estimates of memory consumption. This discrepancy between the calculations can be explained by the use of additional logic to test the operation of the network on the FPGA whose modeling also takes logical and memory resources.

VII. COMPARISON AND ANALYSIS OF ROUTING ALGORITHMS

We also made a comparative analysis of the hardware resources consumption for our algorithm and for the Table routing algorithm, Clockwise algorithm, Adaptive algorithm, and Algorithm for circulants of type $C(N; d, d+1)$ from [5], [14] previously realized in NoCs with two-dimensional circulant topology.

It should be noted that the Clockwise algorithm does not guarantee the optimal path, and the Adaptive algorithm, in order to be optimal, requires a more complicated computational procedure when paths with more than 2 cycles are available.

Besides, it is difficult to speak about comparison in terms of time complexity here since the algorithms from [5], [14] are one-hop algorithms. Unlike the Pair exchange algorithm, where the path is calculated at first node, and at the intermediate ones, there is only a modification of the vector of the shortest paths A^{ij} , in the algorithms under consideration, each next hop is calculated separately at each intermediate node with time complexity equal to $O(1)$. Therefore, it can be argued that the complexity of all algorithms is comparable, but this gives little information in the context of their implementation in RTL. Much more information is provided by the dependence of occupied memory resources (REG) and logical blocks (ALM) on the number of nodes in the network.

Time-dependent parameters could be also measured as the resulting time delay of the combinational circuit which implements the algorithm, but it greatly depends on specific parameters like chip manufacturing technology, floorplaning procedure, etc. and cannot be a universal metric. Moreover, if we assume that the digital automaton implements the algorithm in one clock cycle, then the number of ALM can be considered time-dependent, and REG – space-dependent metric.

The results of implementation on RTL for the proposed algorithm and the algorithms from [5], [14] are given in Tables 5 and 6.

As a result of comparing the algorithms, we can conclude that the proposed pair exchange algorithm takes less logical resources than the adaptive algorithm in [5] and the algorithm for circulants of type $C(N; d, d+1)$ in [14]. In [5] it has been shown that ALMs are a more critical resource than RAMs which limit the number of possible routers in NoCs. At the same time, the pair exchange algorithm is inferior to the adaptive algorithm in terms of used memory resources and it is slightly better than the algorithm for circulants of type $C(N; d, d+1)$. When necessary to minimize the use of memory registers, it is preferable to use the adaptive algorithm, but then the consumption of logical resources in comparison with

TABLE 5. Dependence of memory resources occupied by different algorithms from [5], [14] and pair exchange algorithm in REG on number of nodes in the network (RTL synthesis results).

Number of nodes	Table routing algorithm (TRA)	Clockwise algorithm (CA)	Adaptive algorithm (AA)	Algorithm for circulants of type $C(N; d, d + 1)$ (AC)	Pair exchange algorithm (PEA)
9	274	102	81	322	371
16	820	315	210	617	757
25	1976	674	301	1284	1158
36	4097	1048	546	2215	1962
49	7442	1738	664	3186	2654
64	12476	2419	988	4506	3965
81	20057	3548	1356	6312	4904
100	30429	4857	1662	8327	6044

TABLE 6. Dependence of logic resources occupied by different algorithms from [5], [14] and pair exchange algorithm in ALM on number of nodes in the network (RTL synthesis results).

Number of nodes	Table routing algorithm (TRA)	Clockwise algorithm (CA)	Adaptive algorithm (AA)	Algorithm for circulants of type $C(N; d, d + 1)$ (AC)	Pair exchange algorithm (PEA)
9	79	99	1933	1511	732
16	187	301	4737	3192	1825
25	536	742	11491	6257	2903
36	1019	1354	22025	10959	5473
49	1707	2042	30709	18318	7899
64	2778	3230	48671	28157	12909
81	4622	4839	65825	42333	16755
100	6106	6324	83938	60516	19226

the pair exchange algorithm increases by more than 4 times. Note that the clockwise algorithm is better than pair algorithm in terms of the memory used and logical registers, but it increases the path of a packet into several times and does not guarantee its successful delivery to the destination node. Moreover, the proposed algorithm, as opposed to some of the routing algorithms from [5], [14], is optimal, i.e. always uses the shortest paths to the packet destination. The analytical method proposed is an exact and deterministic; by using analytical formulas, it determines a set of the shortest paths between any two nodes of a network in pair routing.

Thus, the use of the analytical method for development of a routing algorithm in NoCs is a sufficient effective solution. But the method is limited to a specialized structure of communication networks. In the present case, we used the best possible family of two-dimensional circulants of maximum connectivity and with minimums of diameter and mean distance under any number of nodes that provides the ability to use it as the basis to construct interconnection networks for NoCs. It would be promising to accept the analytical method of computing the shortest paths for development of routing algorithms in NoCs with a topology of other known families of three-dimensional and of higher dimension circulant graphs [1]–[3], [20], [24].

VIII. CONCLUSIONS

Thus, the problem of finding the shortest paths in optimal two-dimensional circulants was analytically solved in this work. The calculations proposed were mathematically proven. The theorems formulated and proved in the work to improve the shortest path search algorithm, developed

for optimal generalized Petersen graphs, were also used. Based on the proposed mathematical apparatus, an effective dynamic pair exchange algorithm in two-dimensional circulants was developed. Since it always uses the shortest paths for routing, it is optimal. The algorithm does not use routing tables and adjacency matrices. Due to calculating a vector of the shortest paths and a possibility to use all the set of the shortest paths to a destination node, it is adapted to node and link failures and to load distribution of nodes. We experimentally showed that the overall time complexity $O(1)$ of the algorithm does not depend on the network size in contrast to other well-known algorithms like Dijkstra's algorithm. Such properties make it a promising solution for the use in NoCs which was confirmed by an experimental study while synthesizing NoC communication subsystems and comparing the consumed hardware resources with those when other previously developed routing algorithms.

REFERENCES

- [1] J. C. Bermond, F. Comellas, and D. F. Hsu, "Distributed loop computer-networks: A survey," *J. Parallel Distrib. Comput.*, vol. 24, no. 1, pp. 2–10, Jan. 1995, doi: [10.1006/jpdc.1995.1002](https://doi.org/10.1006/jpdc.1995.1002).
- [2] F. K. Hwang, "A survey on multi-loop networks," *Theor. Comput. Sci.*, vol. 299, nos. 1–3, pp. 107–121, 2003, doi: [10.1016/S0304-3975\(01\)00341-3](https://doi.org/10.1016/S0304-3975(01)00341-3).
- [3] E. A. Monakhova, "A survey on undirected circulant graphs," *Discrete Math., Algorithms Appl.*, vol. 4, no. 1, Mar. 2012, Art. no. 1250002, doi: [10.1142/S1793830912500024](https://doi.org/10.1142/S1793830912500024).
- [4] H. Perez-Roses and S. Department of Mathematics University of Lleida, "Algebraic and computer-based methods in the undirected degree/diameter problem—A brief survey," *Electron. J. Graph Theory Appl.*, vol. 2, no. 2, pp. 166–190, 2014, doi: [10.5614/ejgta.2014.2.2.9](https://doi.org/10.5614/ejgta.2014.2.2.9).
- [5] A. Y. Romanov, "Development of routing algorithms in networks-on-chip based on ring circulant topologies," *Heliyon*, vol. 5, no. 4, Apr. 2019, Art. no. e01516, doi: [10.1016/j.heliyon.2019.e01516](https://doi.org/10.1016/j.heliyon.2019.e01516).

- [6] V. Vilfred, "A few properties of circulant graphs: self-complementary, isomorphism, Cartesian product and factorization," in *Proc. 7th Int. Conf. Modeling, Simulation, Appl. Optim. (ICMSAO)*, Apr. 2017, pp. 1–5, doi: [10.1109/ICMSAO.2017.7934923](https://doi.org/10.1109/ICMSAO.2017.7934923).
- [7] J.-Y. Cai, G. Havas, B. Mans, A. Nerurkar, J.-P. Seifert, and I. Shparlinski, "On routing in circulant graphs," in *Proc. Int. Comput. Combinatorics Conf.* (Lecture Notes in Computer Science), vol. 1627. Tokyo, Japan: Springer-Verlag, 1999, pp. 360–369, doi: [10.1007/3-540-48686-0_36](https://doi.org/10.1007/3-540-48686-0_36).
- [8] B. Robic, "Optimal routing in 2-jump circulant networks," Univ. Cambridge, Cambridge, U.K., Tech. Rep. N397, 1996, p. 7.
- [9] T. Dobravec, J. Žerovnik, and B. Robič, "An optimal message routing algorithm for circulant networks," *J. Syst. Archit.*, vol. 52, no. 5, pp. 298–306, May 2006, doi: [10.1016/j.sysarc.2005.12.003](https://doi.org/10.1016/j.sysarc.2005.12.003).
- [10] J. Zerovnik, B. Robic, and T. Dobravec, "Optimal permutation routing in 2-jump circulant networks," in *Proc. 1st Int. Conf. Softw. Eng. Appl. Netw. Parallel/Distrib. Comput. (SNPD)*, 2000, pp. 175–180.
- [11] D. Gómez, J. Gutierrez, and Á. Ibeas, "Optimal routing in double loop networks," *Theor. Comput. Sci.*, vol. 381, nos. 1–3, pp. 68–85, Aug. 2007, doi: [10.1016/j.tcs.2007.04.002](https://doi.org/10.1016/j.tcs.2007.04.002).
- [12] B.-X. Chen, J.-X. Meng, and W.-J. Xiao, "A constant time optimal routing algorithm for undirected double-loop networks," in *Proc. 1st Int. Conf. Mobile Ad-Hoc Sensor Netw.*, vol. 16. 2005, pp. 308–316, doi: [10.1007/11599463_31](https://doi.org/10.1007/11599463_31).
- [13] C. Mart, R. Beivide, E. Stafford, M. Moret, and E. M. Gabidulin, "Modeling toroidal networks with the Gaussian integers," *IEEE Trans. Comput.*, vol. 57, no. 8, pp. 1046–1056, Aug. 2008, doi: [10.1109/TC.2008.57](https://doi.org/10.1109/TC.2008.57).
- [14] A. Y. Romanov, E. V. Lezhnev, A. Y. Glukhikh, and A. A. Amerikanov, "Development of routing algorithms in networks-on-chip based on two-dimensional optimal circulant topologies," *Heliyon*, vol. 6, no. 1, Jan. 2020, Art. no. e03183, doi: [10.1016/j.heliyon.2020.e03183](https://doi.org/10.1016/j.heliyon.2020.e03183).
- [15] E. A. Monakhova, "Ob analiticheskom opisaniy optimal'nyh dvumernykh diofantovykh struktur odnorodnykh vychislitel'nyh sistem," *Vychislitel'nye Sist.*, no. 90, pp. 81–91, 1981.
- [16] J.-C. Bermond, G. Iliades, and C. Peyrat, "An optimization problem in distributed loop computer networks," *Ann. New York Acad. Sci.*, vol. 555, no. 1, pp. 45–55, May 1989, doi: [10.1111/j.1749-6632.1989.tb22436.x](https://doi.org/10.1111/j.1749-6632.1989.tb22436.x).
- [17] F. Boesch and J.-F. Wang, "Reliable circulant networks with minimum transmission delay," *IEEE Trans. Circuits Syst.*, vol. 32, no. 12, pp. 1286–1291, Dec. 1985, doi: [10.1109/TCS.1985.1085667](https://doi.org/10.1109/TCS.1985.1085667).
- [18] J. L. A. Yebra, M. A. Fiol, P. Morillo, and I. Alegre, "The diameter of undirected graphs associated to plane tessellations," *Ars Combinatoria*, vol. 20, pp. 159–172, 1985.
- [19] R. Beivide, E. Herrada, J. L. Balcasar, and A. Arruabarrena, "Optimal distance networks of low degree for parallel computers," *IEEE Trans. Comput.*, vol. 40, no. 10, pp. 1109–1124, Oct. 1991, doi: [10.1109/12.93744](https://doi.org/10.1109/12.93744).
- [20] A. L. Liestman, J. Opatry, and M. Zaragoza, "Network properties of double and triple fixed step graphs," *Int. J. Found. Comput. Sci.*, vol. 9, no. 1, pp. 57–76, Mar. 1998, doi: [10.1142/S0129054198000076](https://doi.org/10.1142/S0129054198000076).
- [21] V. Puente, C. Izu, R. Beivide, J. A. Gregorio, F. Vallejo, and J. M. Prellezo, "The adaptive bubble router," *J. Parallel Distrib. Comput.*, vol. 61, no. 9, pp. 1180–1208, 2001, doi: [10.1006/jpdc.2001.1746](https://doi.org/10.1006/jpdc.2001.1746).
- [22] V. Puente, C. Izu, J. A. Gregorio, R. Beivide, J. M. Prellezo, and F. Vallejo, "Improving parallel system performance by changing the arrangement of the network links," in *Proc. 14th Int. Conf. Supercomput. (ICS)*, 2000, pp. 44–53, doi: [10.1145/335231.335236](https://doi.org/10.1145/335231.335236).
- [23] Y. L. Yang, H. Amano, H. Shibamura, and T. Sueyoshi, "Recursive diagonal torus: An interconnection network for massively parallel computers," in *Proc. 5th IEEE Symp. Parallel Distrib. Process.*, Dec. 1993, pp. 591–594, doi: [10.1109/spdp.1993.395481](https://doi.org/10.1109/spdp.1993.395481).
- [24] C. Martínez, E. Stafford, R. Beivide, and E. M. Gabidulin, "Modeling hexagonal constellations with eisenstein-jacobi graphs," *Problems Inf. Transmiss.*, vol. 44, no. 1, pp. 1–11, Mar. 2008, doi: [10.1134/S0032946008010018](https://doi.org/10.1134/S0032946008010018).
- [25] R. Beivide, C. Martinez, C. Izu, J. Gutierrez, J. A. Gregorio, and J. Miguel-Alonso, "Chordal topologies for interconnection networks," in *Lect. Notes Comput. Sci. (including Subser. Lect. Notes Artif. Intell. Lect. Notes Bioinformatics)*, vol. 2858. Tokyo, Japan: Springer-Verlag, 2003, pp. 385–392, doi: [10.1007/978-3-540-39707-6_33](https://doi.org/10.1007/978-3-540-39707-6_33).
- [26] M. M. H. Rahman, M. N. M. Ali, A. A. Ibrahim, D. K. Behera, Y. Miura, and Y. Inoguchi, "A new static cost-effective parameter for interconnection networks of massively parallel computer systems," in *Advances in Intelligent Systems and Computing*, vol. 758. Berlin, Germany: Springer-Verlag, 2018, pp. 147–155, doi: [10.1007/978-981-13-0514-6_15](https://doi.org/10.1007/978-981-13-0514-6_15).
- [27] M. M. H. Rahman, F. Al Faisal, R. M. Nor, T. M. T. Sembok, D. K. Behera, and Y. Inoguchi, "Cost effectiveness analysis of a vertical midmesh-connected mesh network (VMMN)," in *Advances in Intelligent Systems and Computing*, vol. 556. Singapore: Springer, 2017, pp. 45–53, doi: [10.1007/978-981-10-3874-7_5](https://doi.org/10.1007/978-981-10-3874-7_5).
- [28] V. K. Jain, T. Ghirmai, and S. Horiguchi, "TESH: A new hierarchical interconnection network for massively parallel computing," *IEICE Trans. Inf. Syst.*, vols. 80, no. 9, pp. 837–846, 1997.
- [29] M. R. Awal, M. M. Hafizur Rahman, and M. A. H. Akhand, "A new hierarchical interconnection network for future generation parallel computer," in *Proc. 16th Int. Conf. Comput. Inf. Technol.*, Mar. 2014, pp. 314–319, doi: [10.1109/ICCITechn.2014.6997341](https://doi.org/10.1109/ICCITechn.2014.6997341).
- [30] E. A. Monakhova, "Algoritmy mezhmashinnykh vzaïmodejstvij i rekonfiguracii grafov svyazey v vychislitel'nykh sistemah s programmiruemoj strukturoj," *Vychislitel'nye Sist.*, no. 94, pp. 81–102, 1982.
- [31] *Cyclone V Device Overview*. Accessed: Sep. 27, 2020. [Online]. Available: <https://www.intel.com/content/www/us/en/programmable/documentation/sam1403480548153.html>



EMILIA A. MONAKHOVA received the M.S. degree in mathematics and applied mathematics from Novosibirsk State University, Novosibirsk, Russia, in 1972 and the Ph.D. degree in computer science from Leningrad Electrotechnical Institute, Leningrad, Russia, in 1988.

She is currently a Senior Scientific Researcher with the System Modeling and Optimization Laboratory, Institute of Computational Mathematics and Mathematical Geophysics, Siberian Branch of the Russian Academy of Sciences (SB RAS), Novosibirsk. She is the author of two books and 80 articles. Her research interests include parallel computer architecture, network topologies, routing algorithms, and evolutionary computation.



ALEKSANDR YU ROMANOV (Member, IEEE), received the B.S. and M.S. degrees in computer systems and networks from National Technical University Kharkov Polytechnic Institute (NTU KhPI), Kharkov, Ukraine, in 2007 and 2009, respectively, and the Ph.D. degree in technical sciences from the Federal State-Funded Institution of Science Institute for Design Problems in Microelectronics of Russian Academy of Sciences (IPPM RAS), Zelenograd, Russia, in 2015.

He is currently an Associate Professor with the Higher School of Economics, National Research University (HSE University), Moscow, and the HSE Tikhonov Moscow Institute of Electronics and Mathematics. He is a university teacher of the following subjects: SoC development and system design of electronic devices. He is the author of the book *Digital Design: Practical Course* and more than 100 scientific articles. His research interests include SoC, NoC, embedded systems, FPGA, and neural networks.

Dr. Romanov has received numerous awards as the supervisor of students' teams taking part in international competitions in programming, electronics, and robotics, as well as the HSE University Best Lecturer Award, in 2018 and 2019.



EVGENII V. LEZHNEV (Member, IEEE) received the B.S. and M.S. degrees in computer networks and systems from the Higher School of Economics, National Research University (HSE University), Moscow, Russia, in 2015 and 2017, respectively, where he is currently pursuing the degree in design automation systems.

Since 2018, he has been working as a research trainee in the Laboratory of Computer aided design systems. Since 2020, he has been an Assistant Teacher with the HSE Tikhonov Moscow Institute of Electronics and Mathematics. He is a coauthor of ten articles. His research activities are related to the study, low-level modeling, and development of NoC topologies. His awards and honors include Golden HSE Award 2019 in the Silver Nestling nomination and the Innovate FPGA (Intel) Award.

• • •